

Notice of References Cited

Application/Control No.

08/915,658

Applicant(s)/Patent Under Reexamination

TRIVEDI, JIGISH D

Examiner

Ginette Peralta

Art Unit

2814

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U.S. PATENT DOCUMENTS

*		DOCUMENT NO	DATE	NAME	CLASS	SUBCLASS	DOCUMENT SOURCE **	
							APS	OTHER
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		DOCUMENT (Including Author, Title Date, Source, and Pertinent Pages)	DOCUMENT SOURCE **	
			APS	OTHER
<input type="checkbox"/>	U	Dao, T.; Svejda, F.J.. A dual-port SRAM compiler for 0.8-mm 110K BiCMOS gate arrays. Custom Integrated Circuits Conference, 1991, Proceedings of the IEEE, 1991 Pp. 22.4/1-22.4/3	<input type="checkbox"/>	<input checked="" type="checkbox"/>
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*A copy of this reference is not being furnished with this Office action. (See Manual of Patent Examining Procedure, Section 707.05(a))

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